



M.S. DELL 2-4-1-1-5-28

FIG. 1
(PRIOR ART)

100

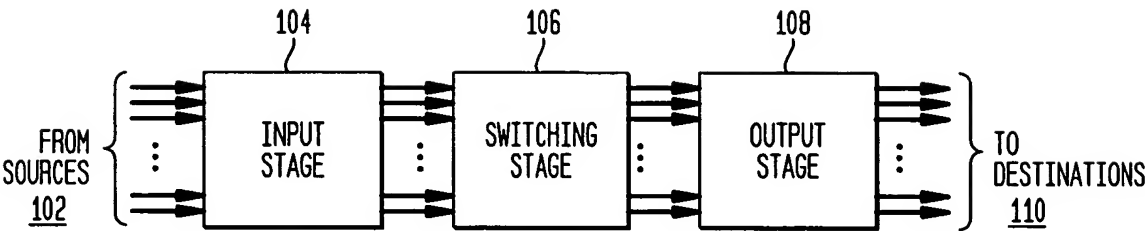


FIG. 2

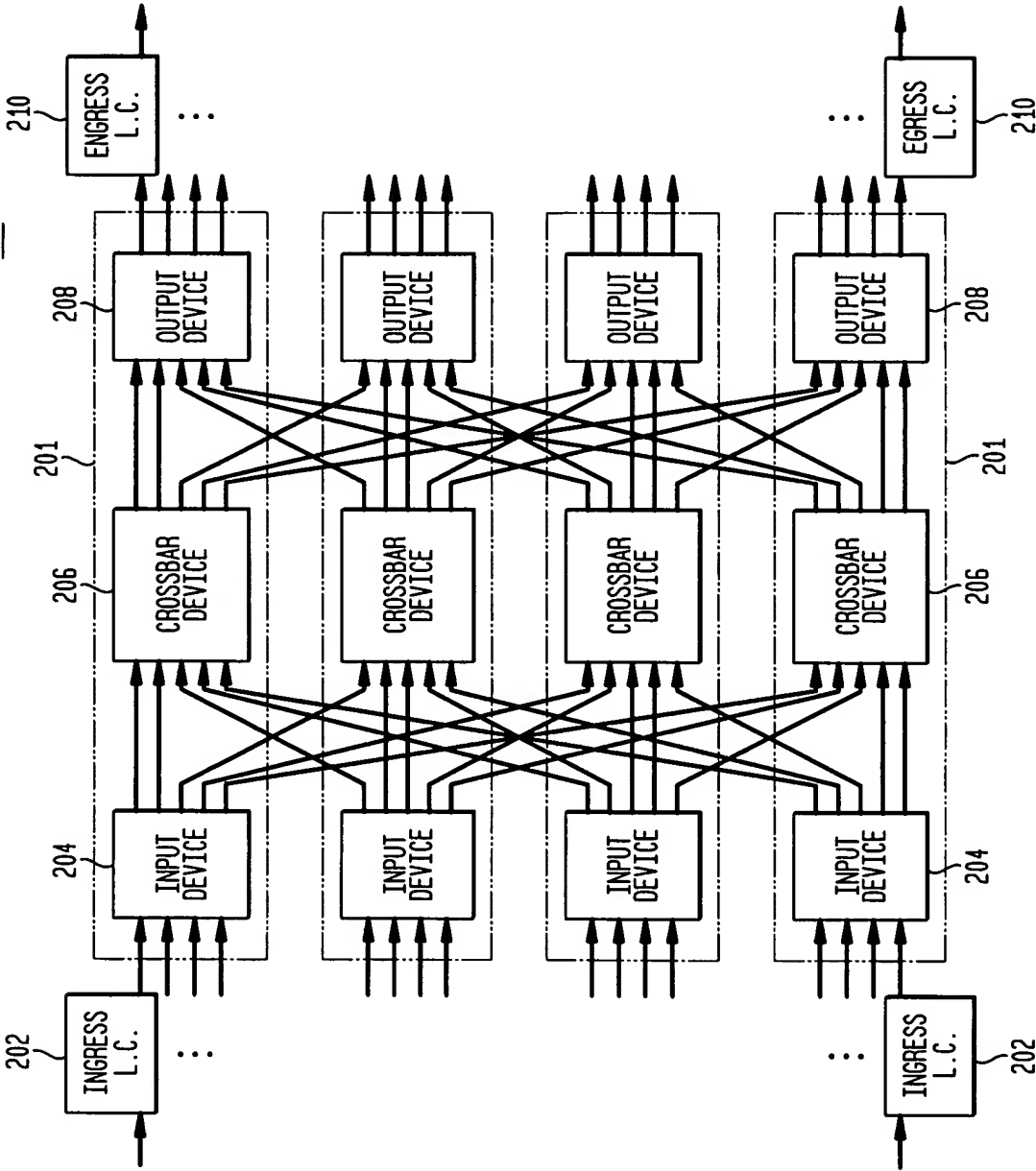


FIG. 3

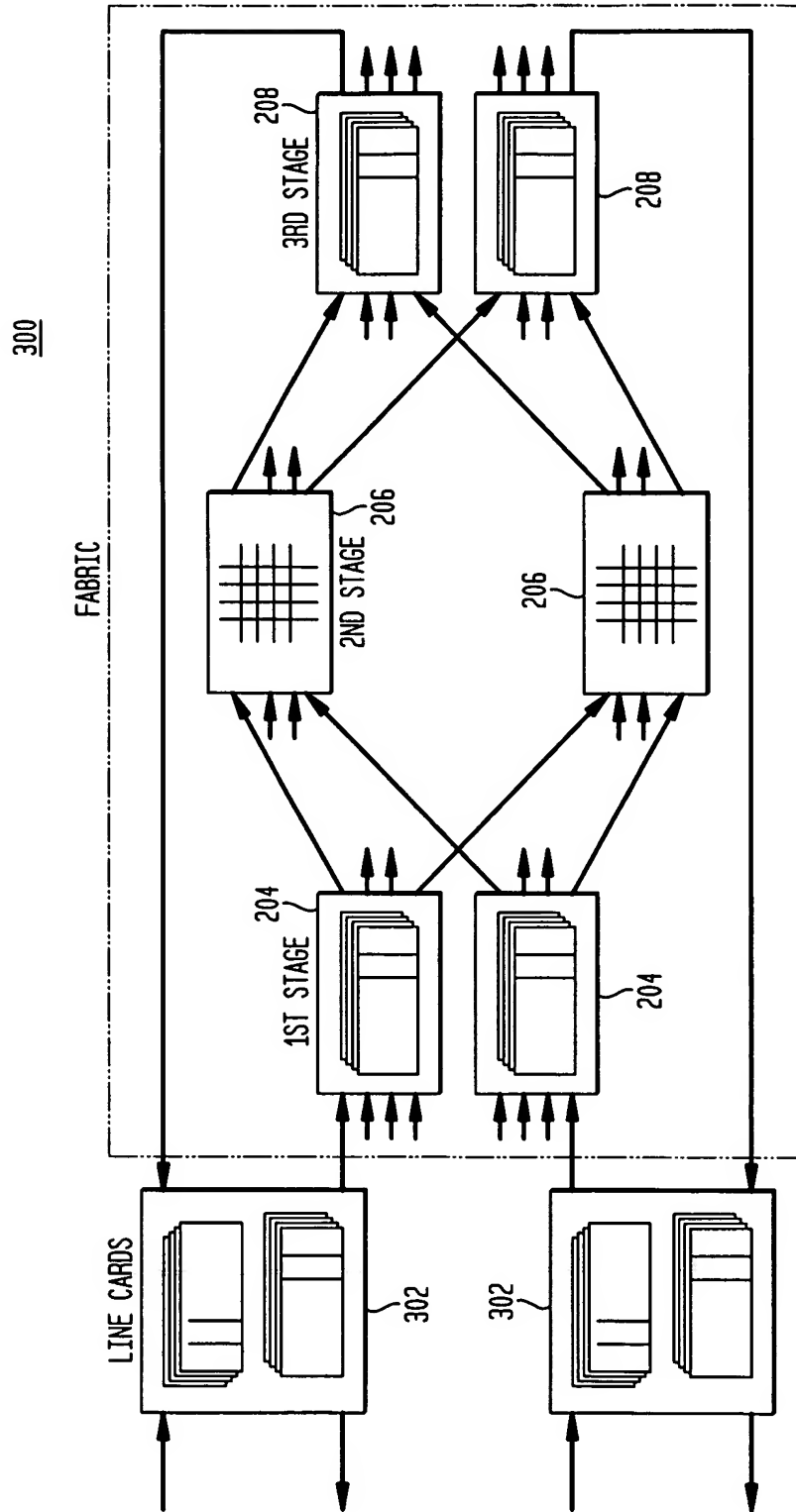


FIG. 4

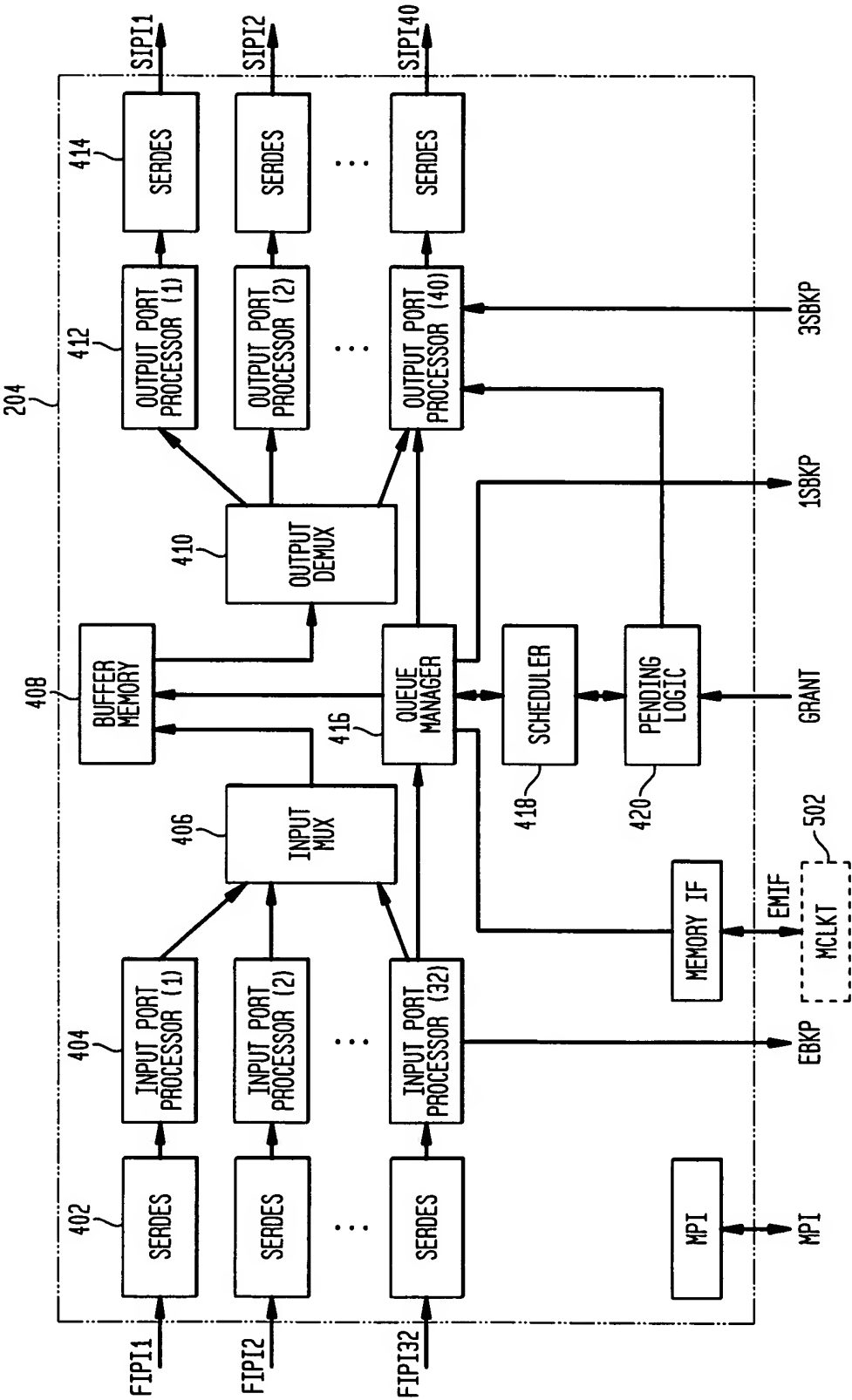


FIG. 5

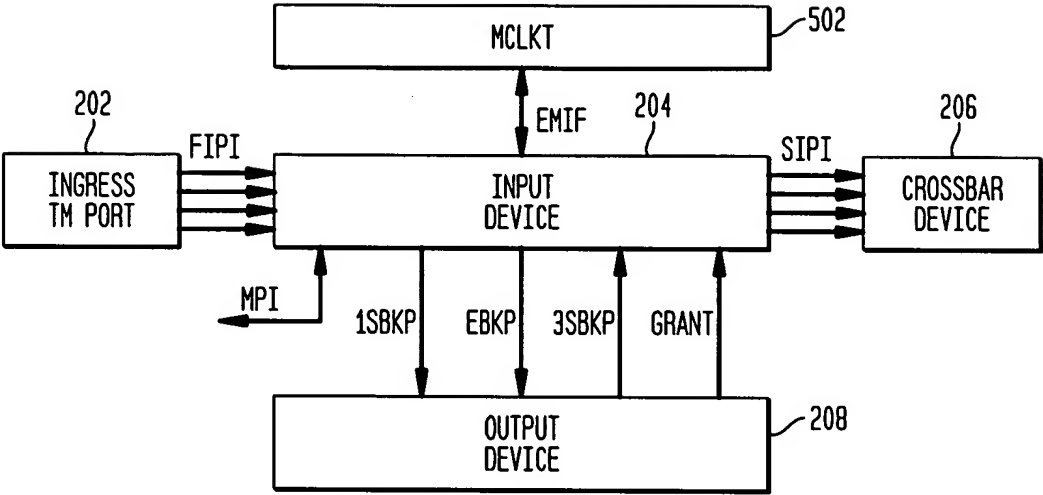
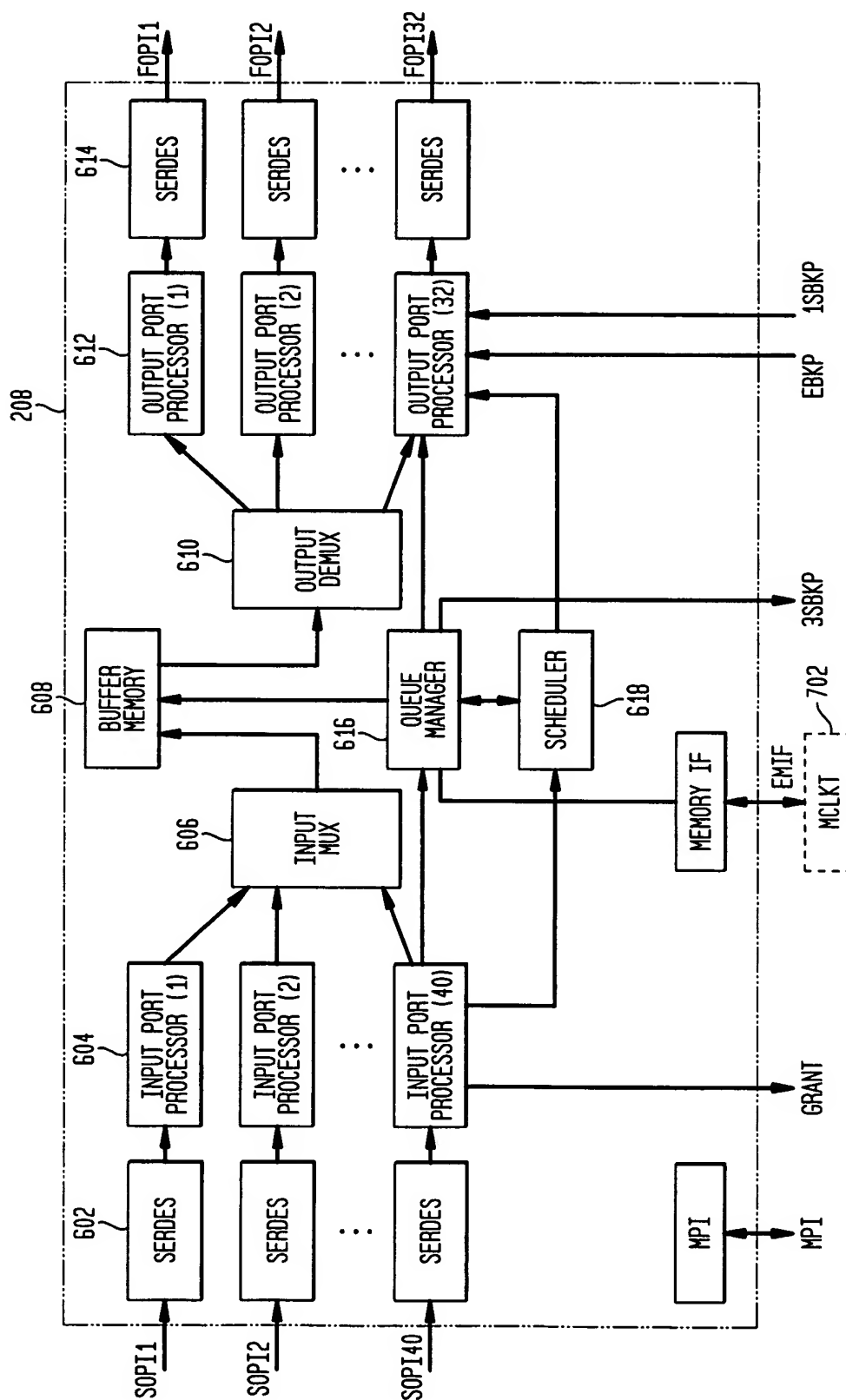


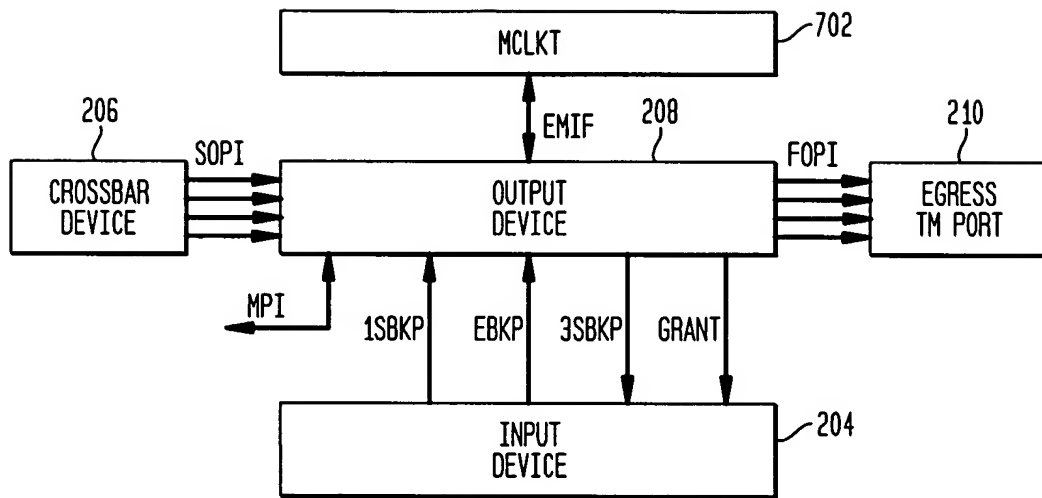
FIG. 6

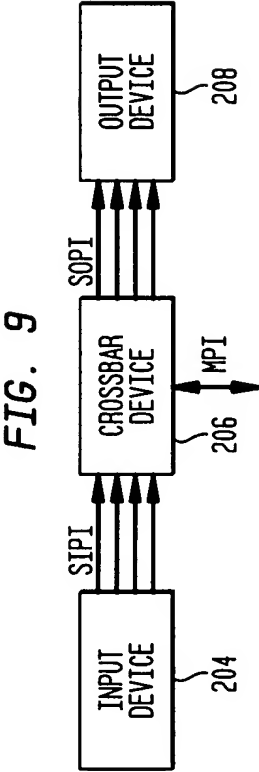
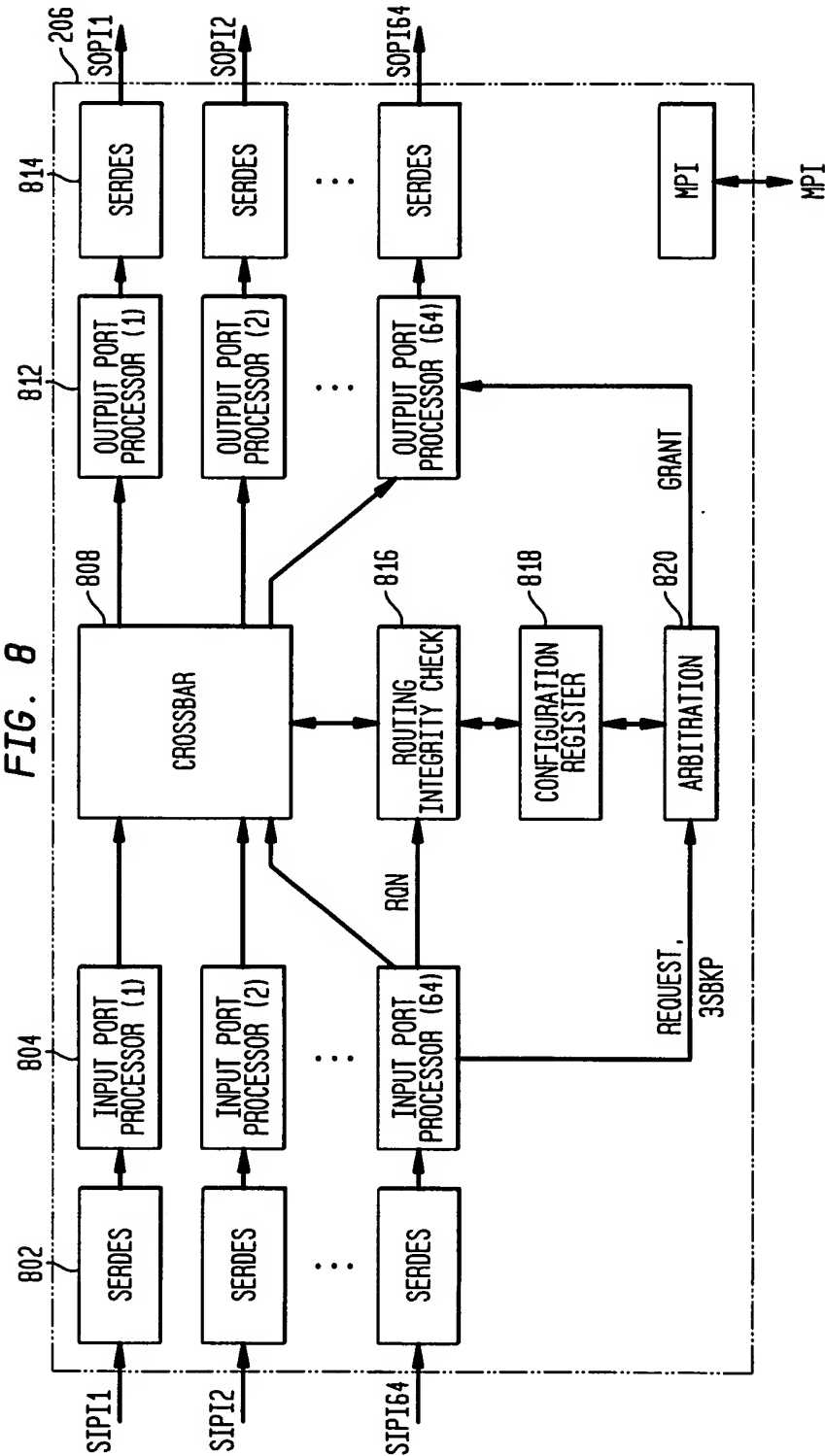


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FIG. 7





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FIG. 10

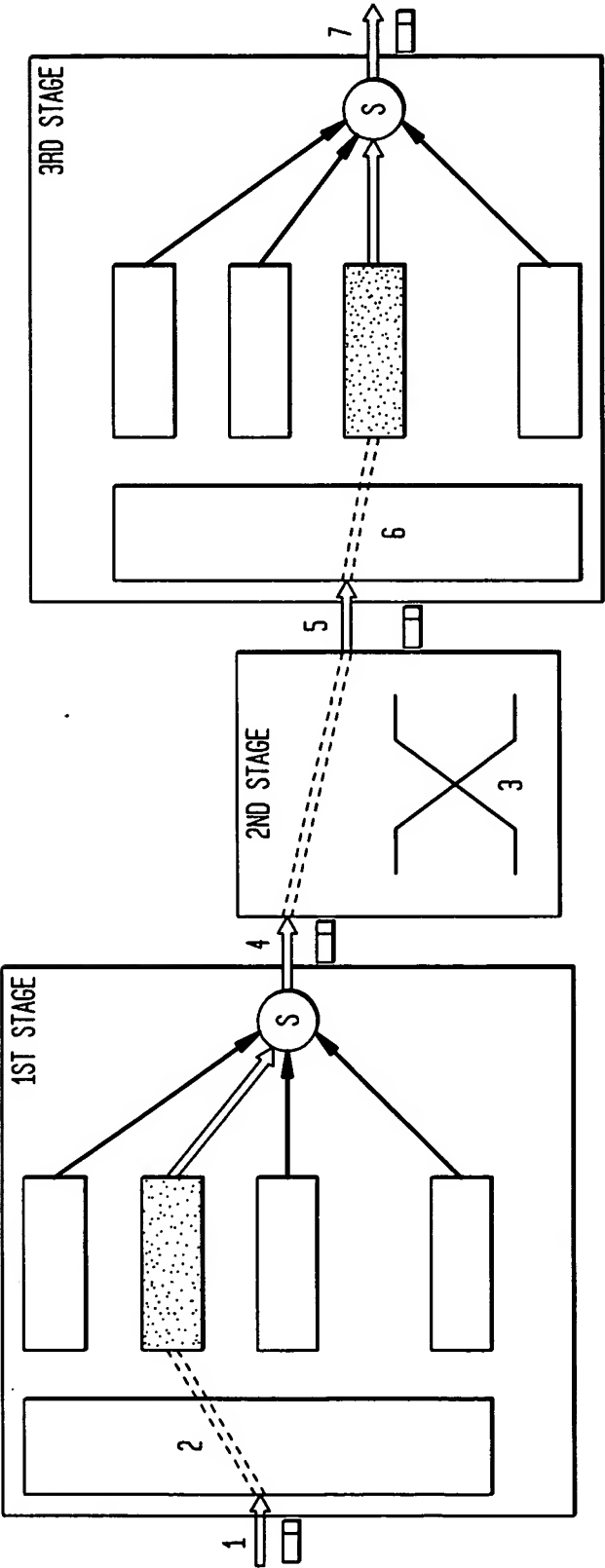
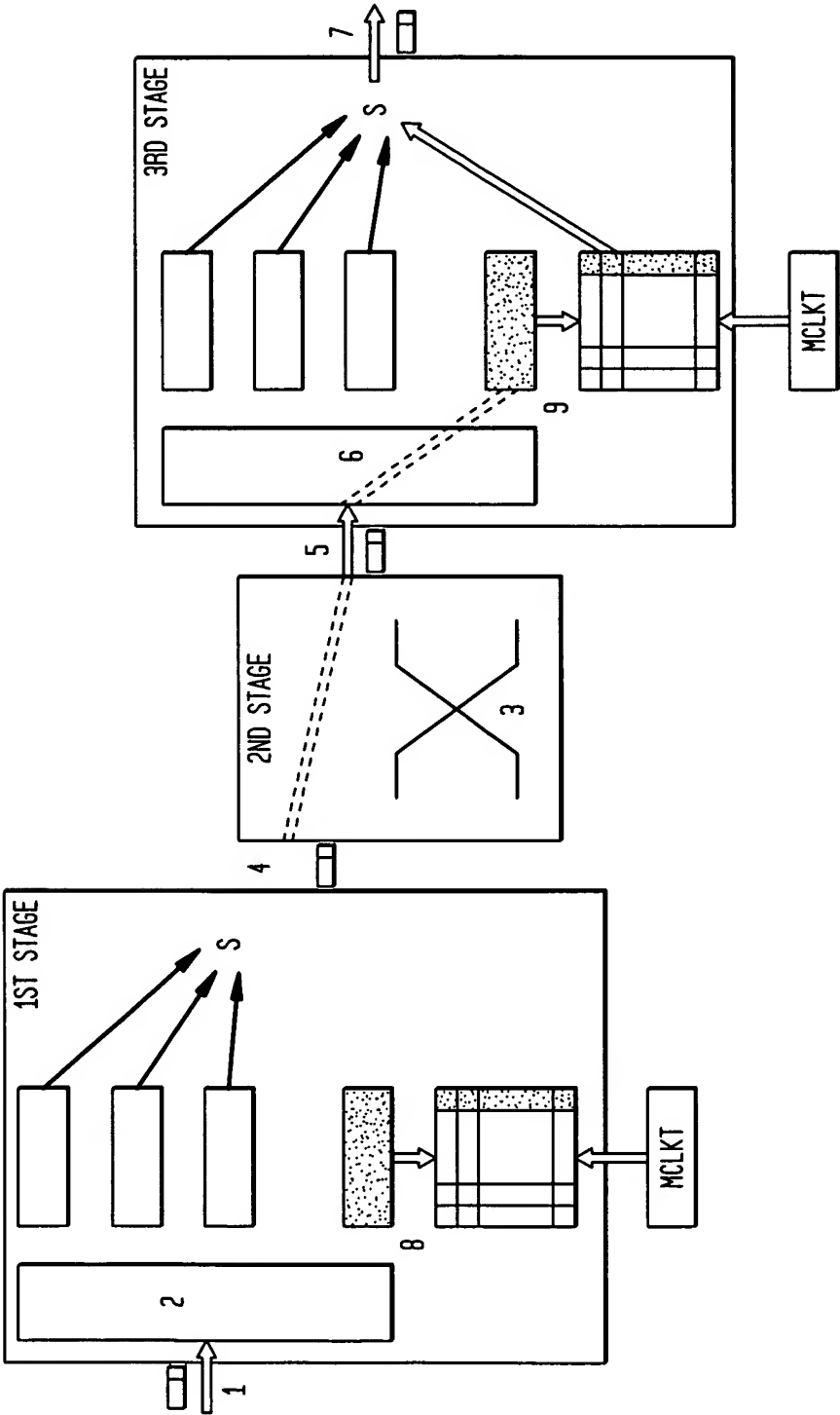
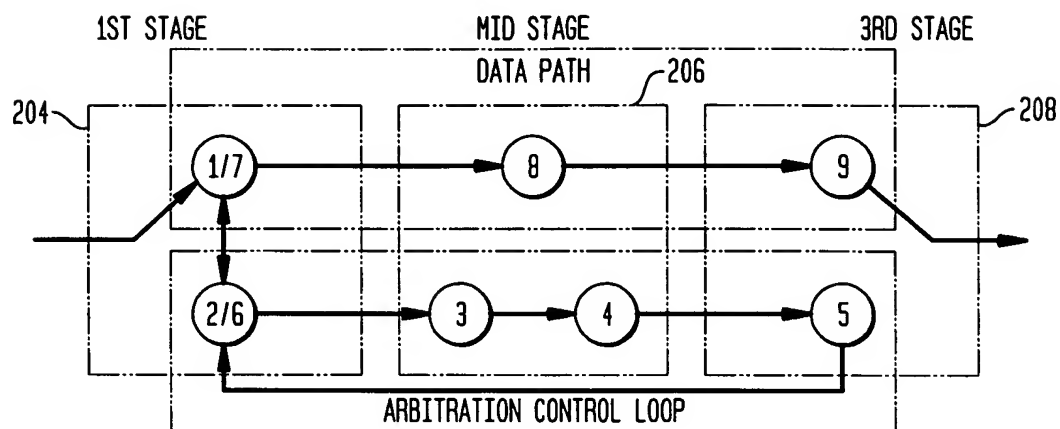


FIG. 11



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FIG. 12**FIG. 13**

IDLEBID(1)	BIDCH(2)	RDESTD(6)	RDESTP(5)	R3QN(10)	RSRCD(6)
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FIG. 14

DATA	SIZE	DESCRIPTION
IDLEBID	1	IDLE BID INDICATOR. IT IS EXTRACTED FROM CELL HEADER (BIDTYPE FIELD). 0 - IDLE BID 1 - VALID BID
BIDCH	2	BID CHANNEL. IT IS EXTRACTED FROM CELL HEADER (BIDTYPE FIELD). 0 - UNICAST GBW CHANNEL 1 - UNICAST BE CHANNEL 2 - MULTICAST GBW CHANNEL 3 - MULTICAST BE CHANNEL
R3QN	10	REQUESTED 3RD STAGE QUEUE NUMBER. IT IS EXTRACTED FROM CELL HEADER (REQRON FIELD).
RDESTD	6	REQUESTED 3RD STAGE DEVICE. IT IDENTIFIES A 3RD STAGE DEVICE. IT IS EXTRACTED FROM CELL HEADER (REQDEST FIELD).
RDESTP	5	REQUESTED 3RD STAGE PORT. IT IDENTIFIES AN EGRESS PORT NUMBER OF A 3RD STAGE DEVICE. IT IS EXTRACTED FROM CELL HEADER (REQDEST FIELD).
RSRCD	6	REQUESTING 1ST STAGE DEVICE. IT IDENTIFIES A 1ST STAGE DEVICE THAT GENERATES THE REQUEST. IT IS DERIVED FROM THE INPUT LINK NUMBER.

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FIG. 15

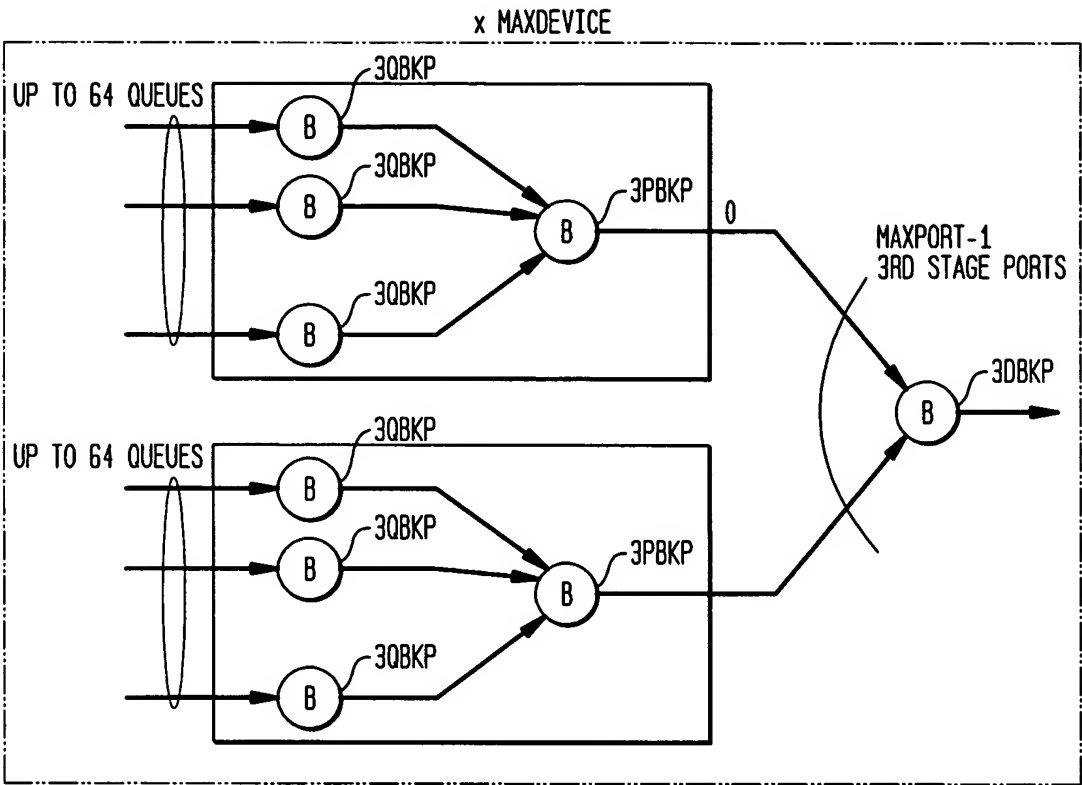
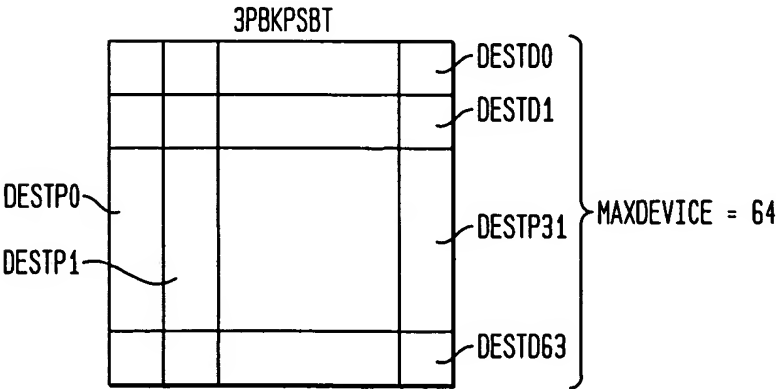


FIG. 16



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FIG. 17

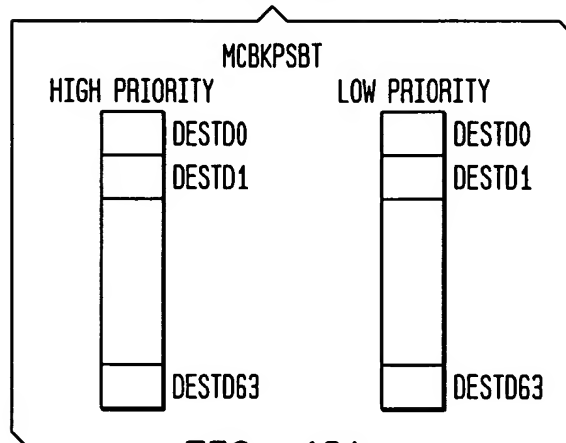


FIG. 18A

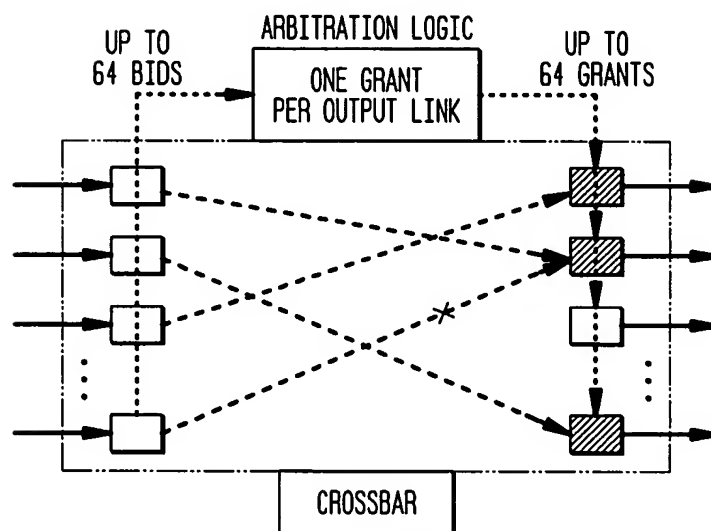
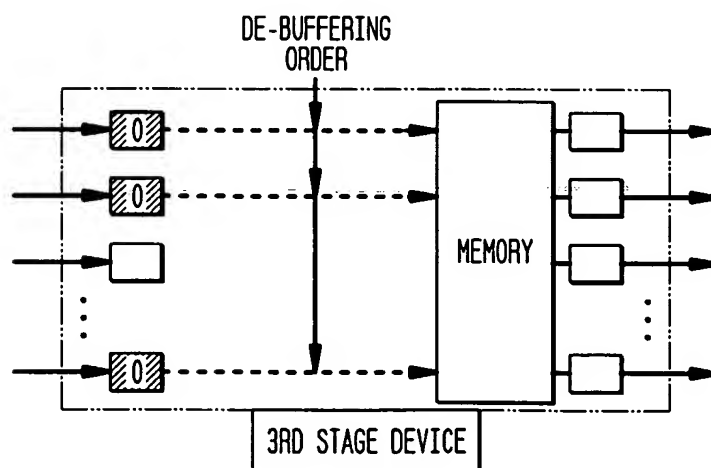


FIG. 18B



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FIG. 19

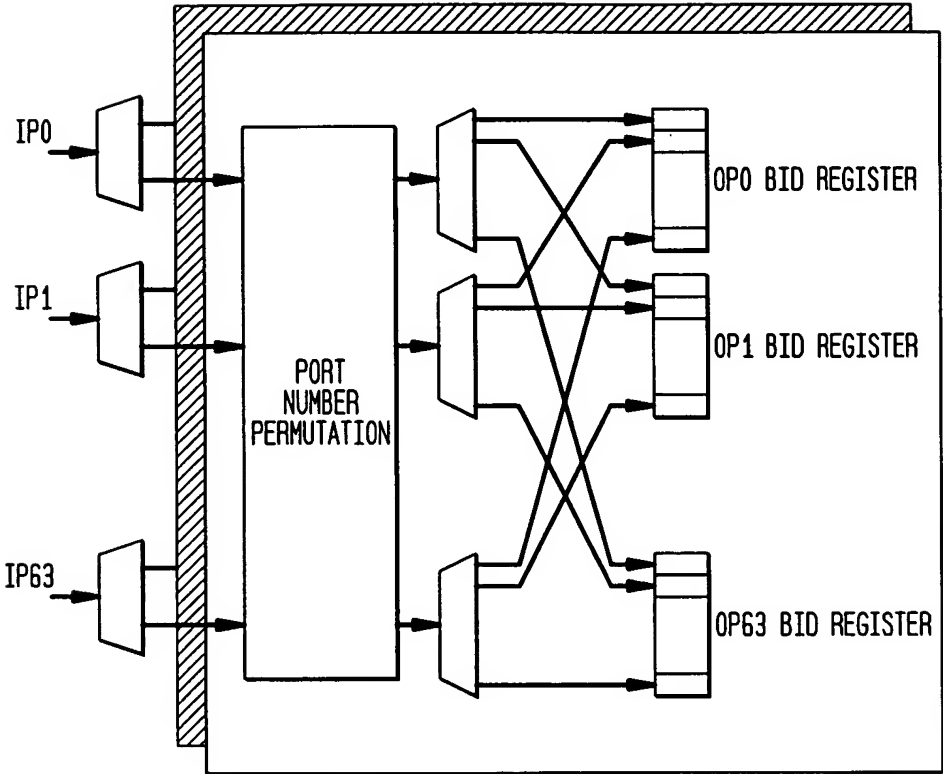


FIG. 20

GRANT (2)	GNTDEST (10)	GNTRON (10)
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FIG. 21

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FOR EACH CROSSBAR DEVICE,  
  FOR EACH TIME SLOT,  
    DENY ALL BIDS TO BACK-PRESSURED PORTS OR QUEUES,  
    DENY ALL LOW-PRIORITY BIDS TO OUTPUT DEVICES WHEN HIGHER PRIORITY BIDS ARE PRESENT,  
    FOR THE REMAINING BIDS,  
      FOR EACH OUTPUT DEVICE,  
        RANDOMLY SELECT ONE OF ANY CONTENDING BIDS FOR GRANT  
      ELSE  
        IF THE BID WAS DENIED DUE TO COLLISION,  
          BID AGAIN TO THE CROSSBAR DEVICE FOR THE SAME OUTPUT DEVICE  
        ELSE  
          BID TO THE CROSSBAR DEVICE FOR THE NEXT OUTPUT DEVICE IN PRE-CONFIGURED SEQUENCE (CROSSBAR)
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FOR EACH INPUT DEVICE,  
  FOR EACH TIME SLOT,  
    FOR EACH CROSSBAR DEVICE,  
      IF THE BID SUBMITTED L SLOTS EARLIER WAS GRANTED,  
        TRANSMIT CELL TO THE OUTPUT DEVICE USING THE CROSSBAR DEVICE  
      ELSE
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FIG. 22

INITIALIZATION:

SET WINDOW \geq # OF LINKS * LATENCY;
CREDIT = WINDOW

SCHEDULER:

WHERE THERE IS A BACKLOG AND CREDIT > 0 ,
SCHEDULE A CELL FROM VoQ;
DECREMENT CREDIT

PENDING LOGIC:

FOR EACH TIME SLOT,
FOR EACH RECEIVED GRANT SIGNAL FROM EACH CROSSBAR DEVICE,
TRANSMIT A CELL;
INCREMENT CREDIT;
SUBMIT BIDS UP TO # OF LINKS FROM THE PENDING QUEUES

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FIG. 23A

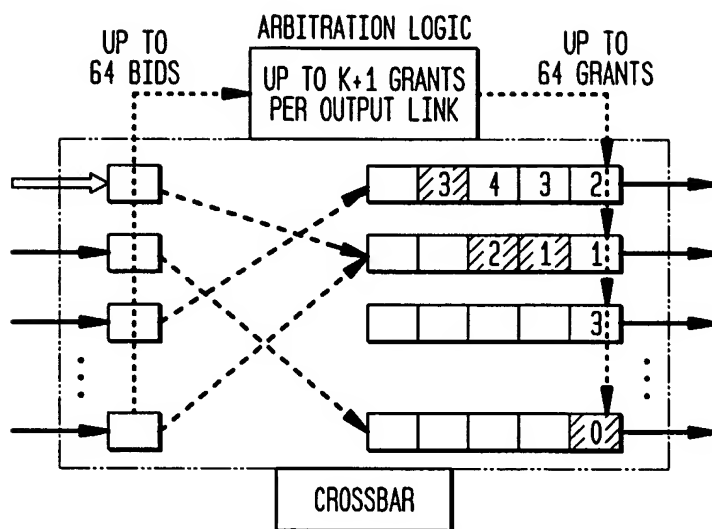


FIG. 23B

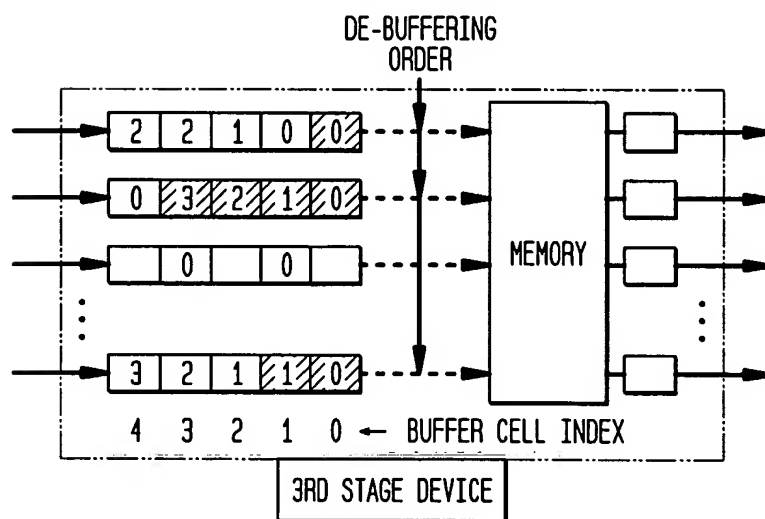


FIG. 24

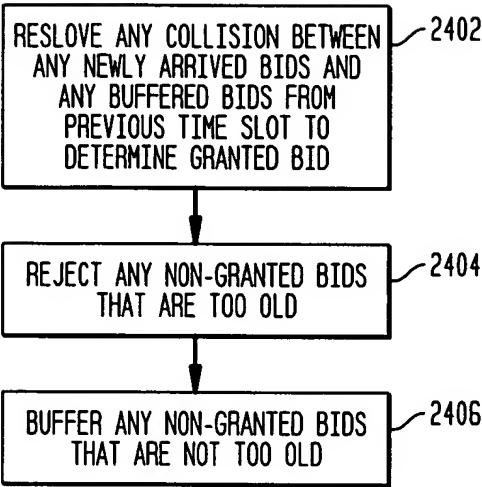


FIG. 25

TABLE 1

PERFORMANCE IMPROVEMENTS WITH BUFFERS AT THE CROSSBAR

K	THROU- GHPUT [%]	MAX 3 GRANTS	MAX 2 GRANTS	GAIN [%]	MAX 3 GRANTS	MAX 2 GRANTS
0	63.5			0.0		
1	78.8			24.1		
2	84.9		83.62	33.7		31.68
3	88.5	88.39	86.18	39.4	39.20	36.69
4	90.8	90.70	87.37	43.0	42.83	37.59
5	92.2	91.64	88.14	45.2	44.34	38.80
6	93.2	92.66	88.82	46.8	45.92	39.87
7	93.9	93.55	88.96	47.9	47.32	40.09
8	94.7	93.98	89.37	49.1	48.00	40.74
9	95.3	94.40	89.66	50.1	48.66	41.20
10	95.7	94.90	89.57	50.7	49.45	41.01
15	97			52.8		
20	98			54.3		
30	99	97	90	55.9	52.76	41.85

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FIG. 26

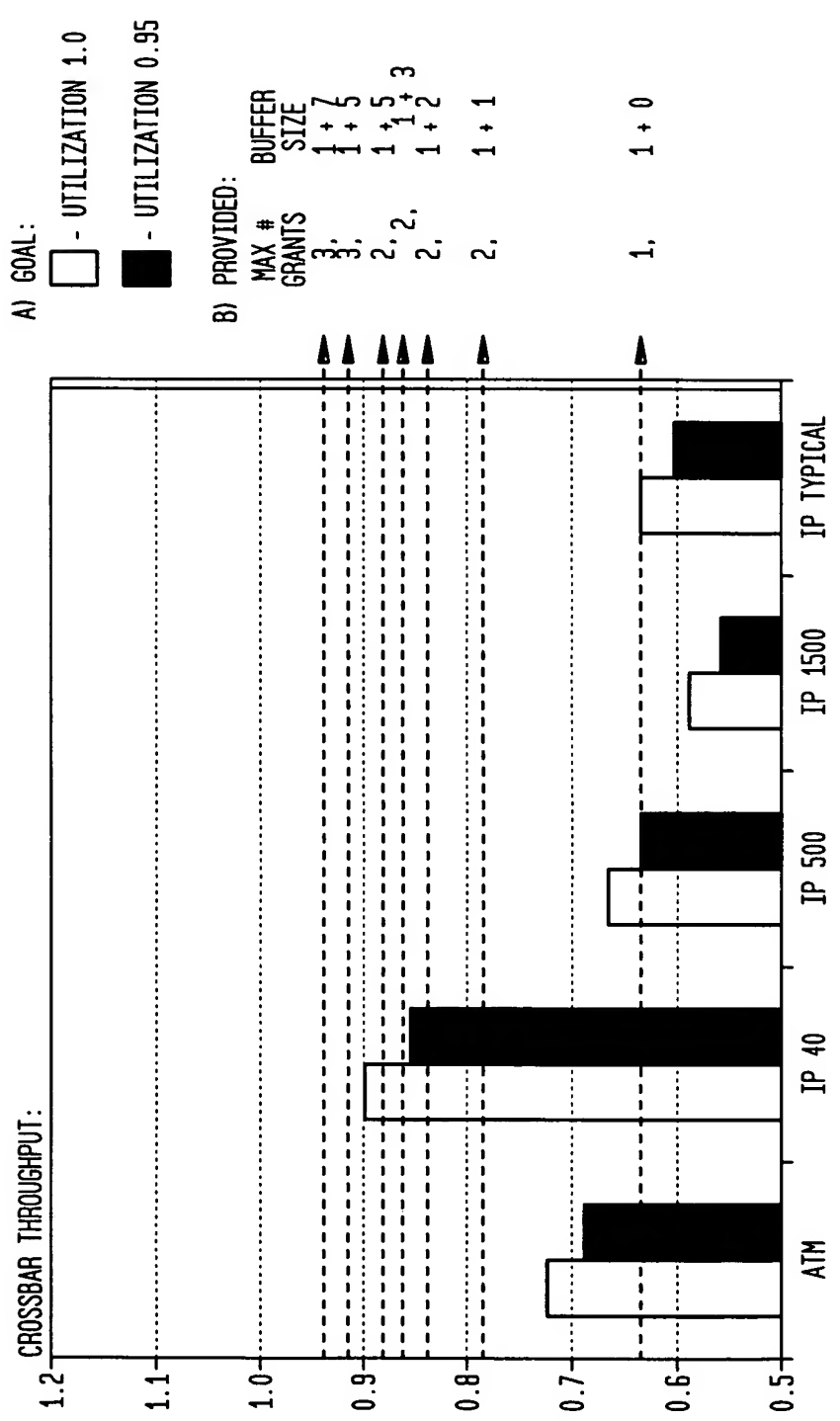


FIG. 27A

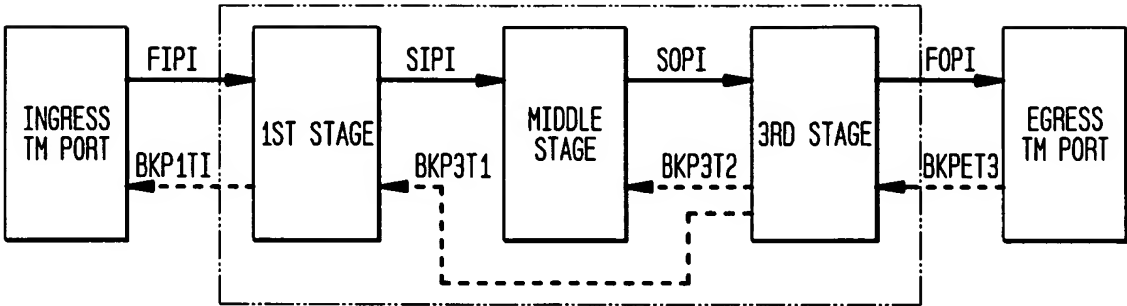


FIG. 27B

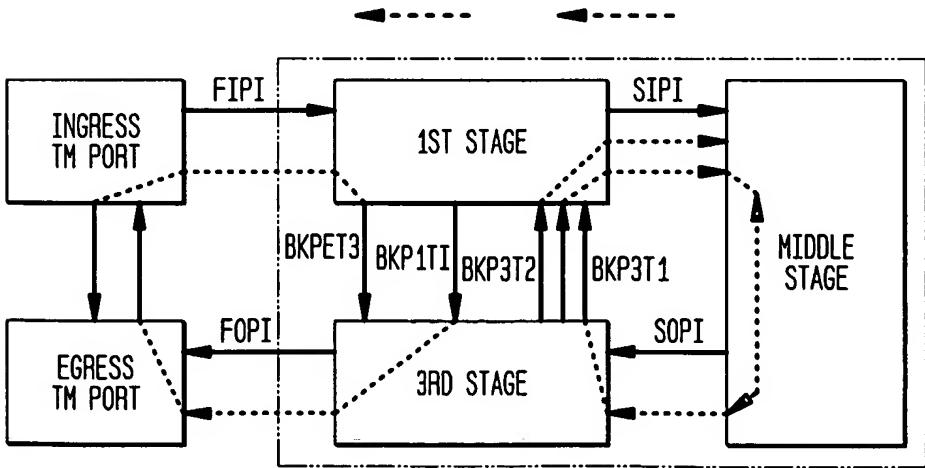


FIG. 28

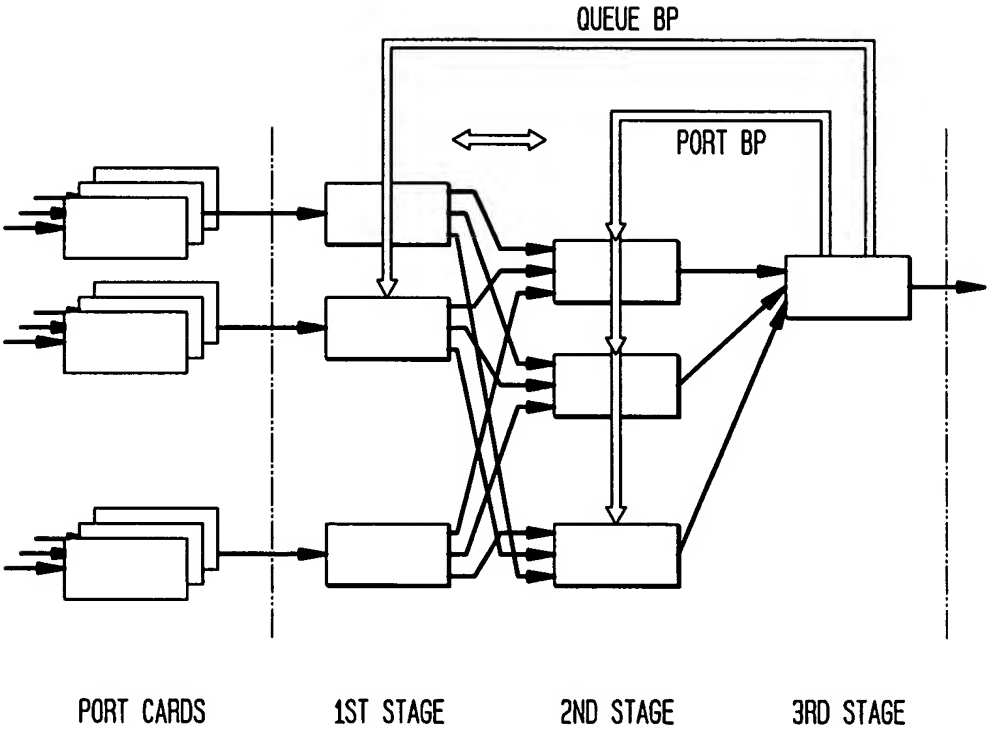


FIG. 29

